



Memory Systems: Cache, DRAM, Disk

Bruce Jacob, Spencer Ng, David Wang

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Is your memory hierarchy stopping your microprocessor from performing at the high level it should be?

Memory Systems: Cache, DRAM, Disk shows you how to resolve this problem.

The book tells you everything you need to know about the logical design and operation, physical design and operation, performance characteristics and resulting design trade-offs, and the energy consumption of modern memory hierarchies. You learn how to tackle the challenging optimization problems that result from the side-effects that can appear at any point in the entire hierarchy.

As a result you will be able to design and emulate the entire memory hierarchy.

- Understand all levels of the system hierarchy -Xcache, DRAM, and disk.
- Evaluate the system-level effects of all design choices.
- Model performance and energy consumption for each component in the memory hierarchy.

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