



HPC@Green IT: Green High Performance Computing Methods

Ralf Gruber, Vincent Keller

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Making the most efficient use of computer systems has rapidly become a leading topic of interest for the computer industry and its customers alike. However, the focus of these discussions is often on single, isolated, and specific architectural and technological improvements for power reduction and conservation, while ignoring the fact that power efficiency as a ratio of performance to power consumption is equally influenced by performance improvements and architectural power reduction. Furthermore, efficiency can be influenced on all levels of today's system hierarchies from single cores all the way to distributed Grid environments. To improve execution and power efficiency requires progress in such diverse fields as program optimization, optimization of program scheduling, and power reduction of idling system components for all levels of the system hierarchy. Improving computer system efficiency requires improving system performance and reducing system power consumption. To research and reach reasonable conclusions about system performance we need to not only understand the architectures of our computer systems and the available array of code transformations for performance optimizations, but we also need to be able to express this understanding in performance models good enough to guide decisions about code optimizations for specific systems. This understanding is necessary on all levels of the system hierarchy from single cores to nodes to full high performance computing (HPC) systems, and eventually to Grid environments with multiple systems and resources.

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